

**NONPROVISIONAL PATENT
APPLICATION TRANSMITTAL RULE §1.53(b)
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Customer No. 004372
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Docket No. 024016-00068
Date: August 27, 2003

Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is a nonprovisional patent application:

For (Title): SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, AND ADJUSTMENT
METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

By (Inventors): Yasushige OGAWA, Yoshiyuki ISHIDA, Masato MATSUMIYA

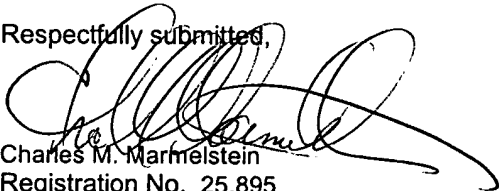
- ☒ 45 pages of Specification/Claims 1-23/Abstract are attached.
- ☒ Formal drawings (Fig(s). 1-18; 17 sheets) are attached.
- ☒ A Declaration and Power of Attorney is attached.
- ☒ An assignment of the invention to FUJITSU, LTD. is attached, along with Form PTO-1595 and a check for \$40.00.
- ☒ An Information Disclosure Statement is attached, along with Form PTO-1449, and 2 reference(s).
- ☒ Priority of foreign application No. 2002-248134 filed August 28, 2002 in Japan and 2003-192151 filed July 4, 2003 in Japan is claimed under 35 U.S.C. §119.
- ☒ Certified copies of the above corresponding foreign applications are attached.

The filing fee is calculated below and includes claim status after entry of any Preliminary Amendment noted above:

			SMALL ENTITY			LARGE ENTITY		
FOR:	NO. FILED	NO. EXTRA	RATE	FEE	OR	RATE	FEE	
BASIC FEE				\$ 375	OR		\$ 750	
TOTAL CLAIMS	23 - 20	= 3	x 9 =	\$	OR	x 18	\$ 54	
INDEP CLAIMS	4 - 3	= 1	x 42 =	\$	OR	x 84	\$ 84	
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+140 =	\$	OR	+280	\$	
			TOTAL	\$	OR	TOTAL	\$ 888	

- ☒ A check in the amount of \$928.00 (\$888 for the filing fee and \$40 for the Assignment Recordation Fee) is attached. Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300.

Respectfully submitted,


Charles M. Marmelstein
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CMM/cam